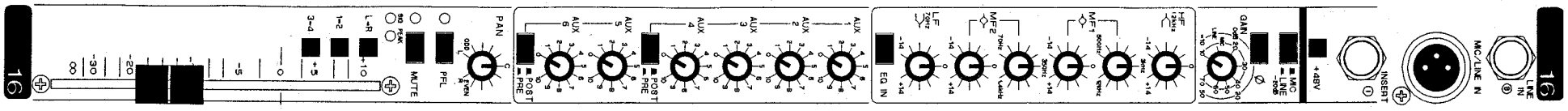
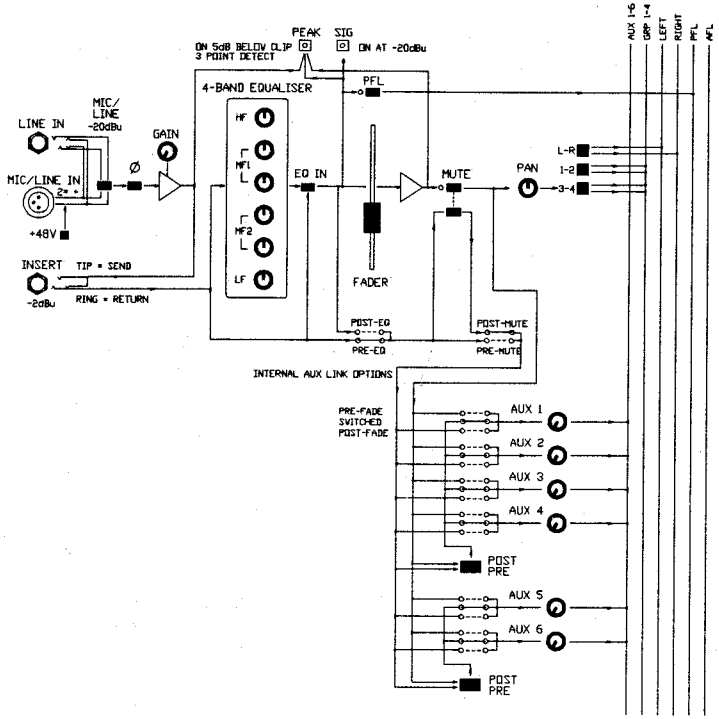
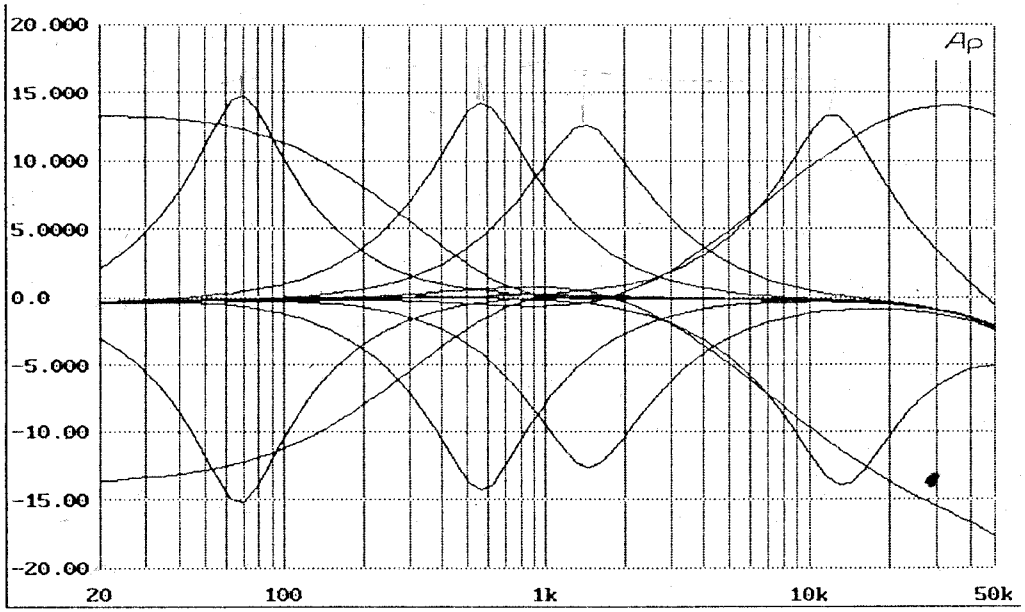
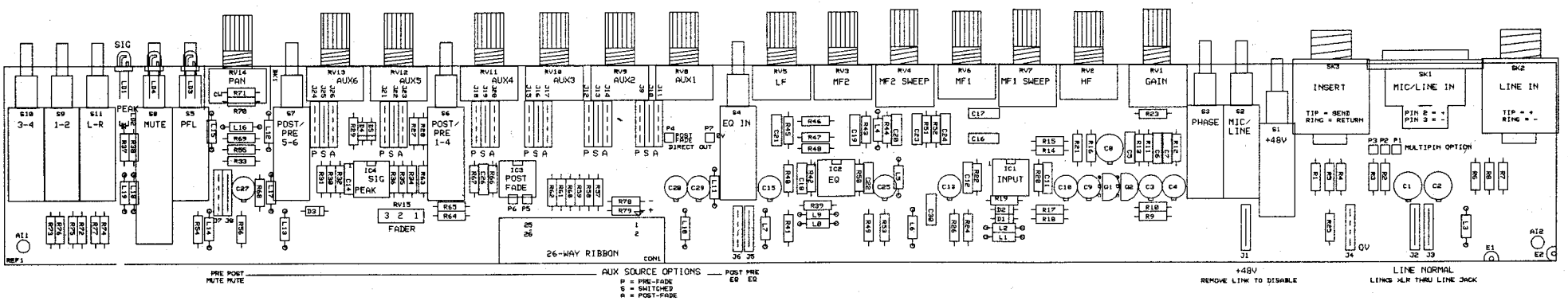


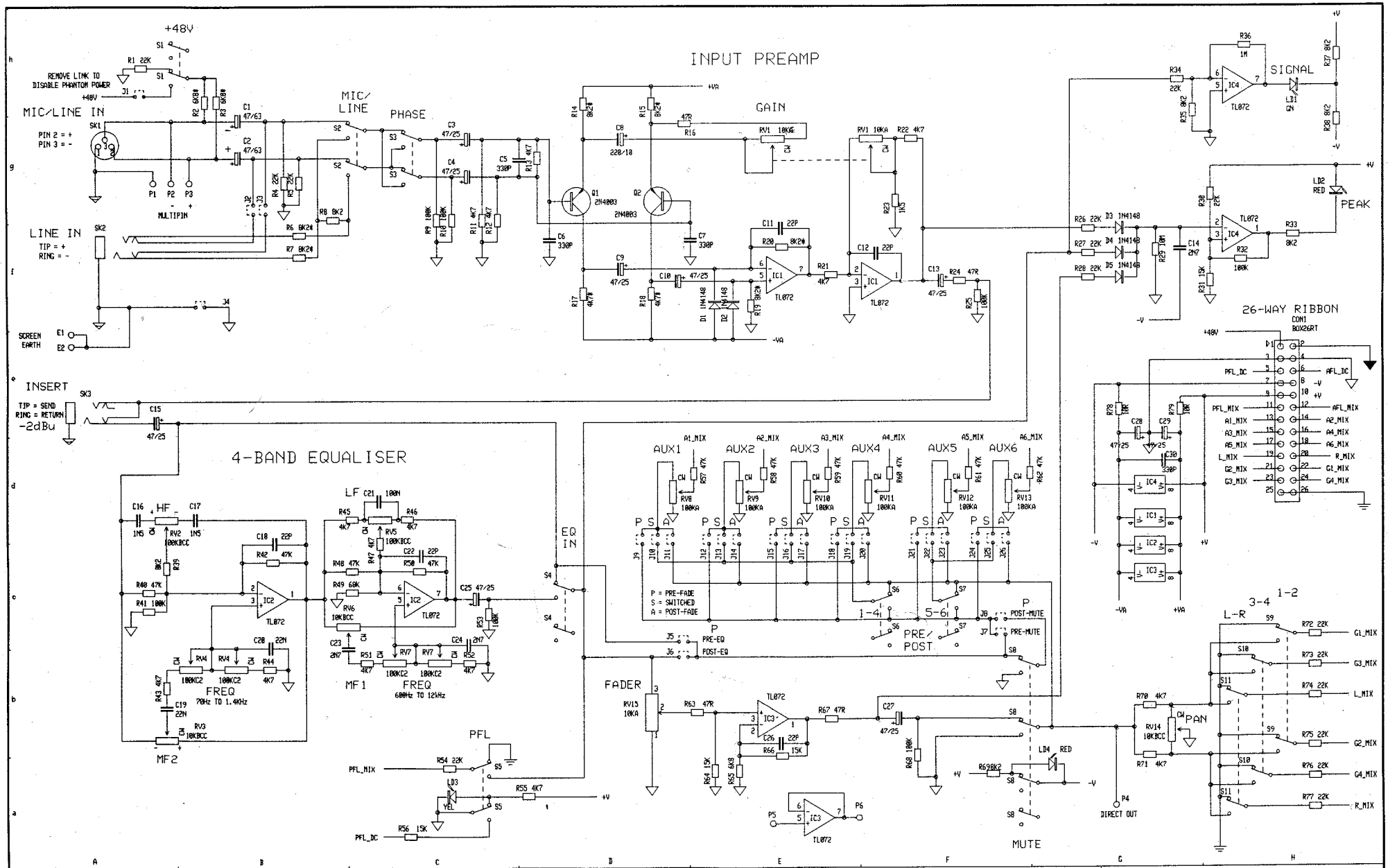


EQUALISER





FW038D GL3 INPUT-4 PCB AG0321 ISSUE 1 COMPONENT LAYOUT
BY CD 11-7-92



ISS.	REVISION	BY	DATE
P1	ORIGIN	CD	1-7-92
1	PRODUCTION	CD	30-8-92

NOTES

1. RESISTORS MARKED # ARE 1%
   ALL OTHERS ARE 5% 1/4W UNLESS OTHERWISE MARKED
2. ELECTROLYTIC CAPACITORS ARE µF/VOLTS

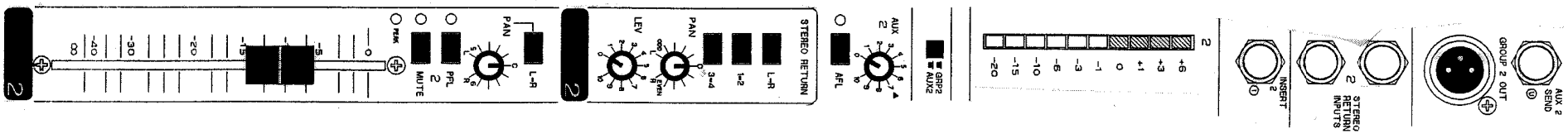
INPUT

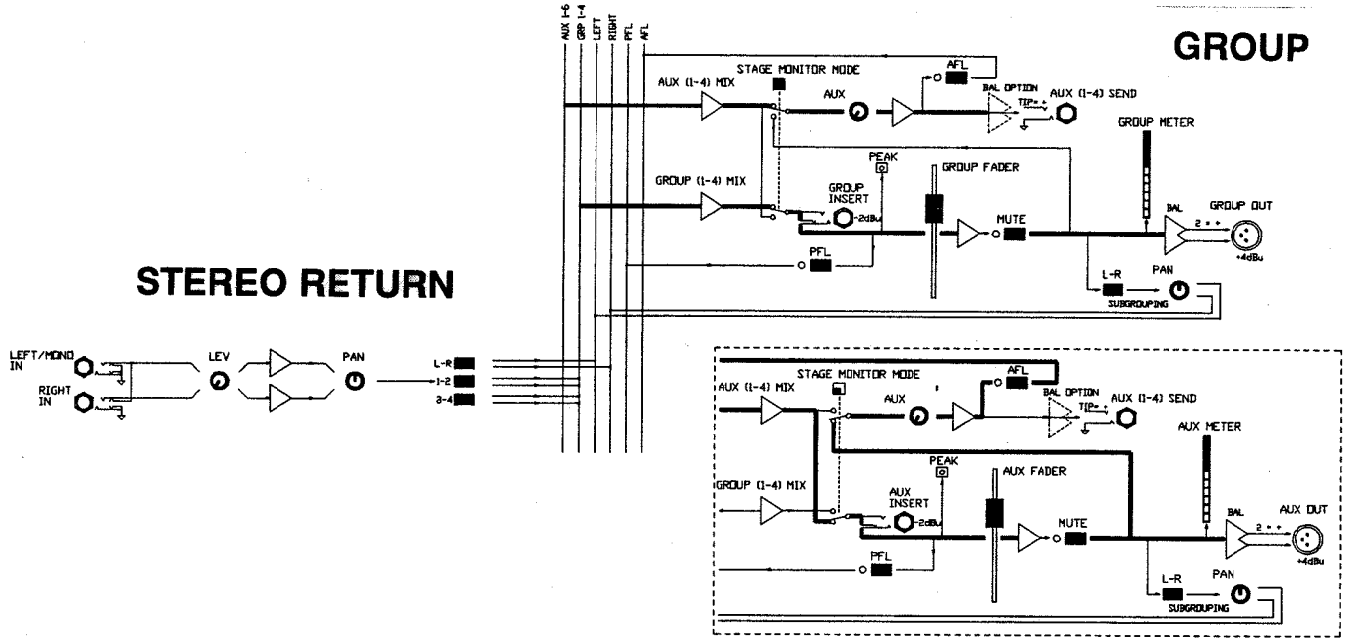
UNIT TITLE: GL3

DRAWING TITLE: INPUT-4 CIRCUIT DIAGRAM
PCB TYPE AG0321

MANUFACTURED IN ENGLAND BY
ALLEN & HEATH

DRAWING No. D176 ISSUE 1

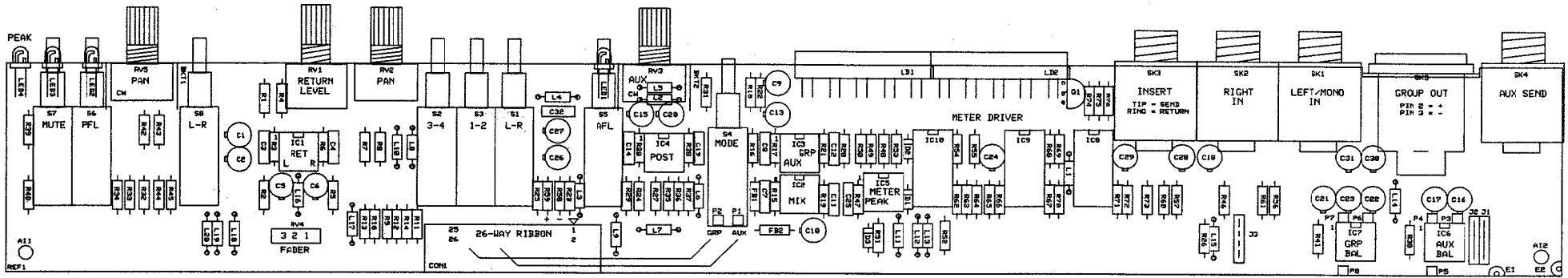




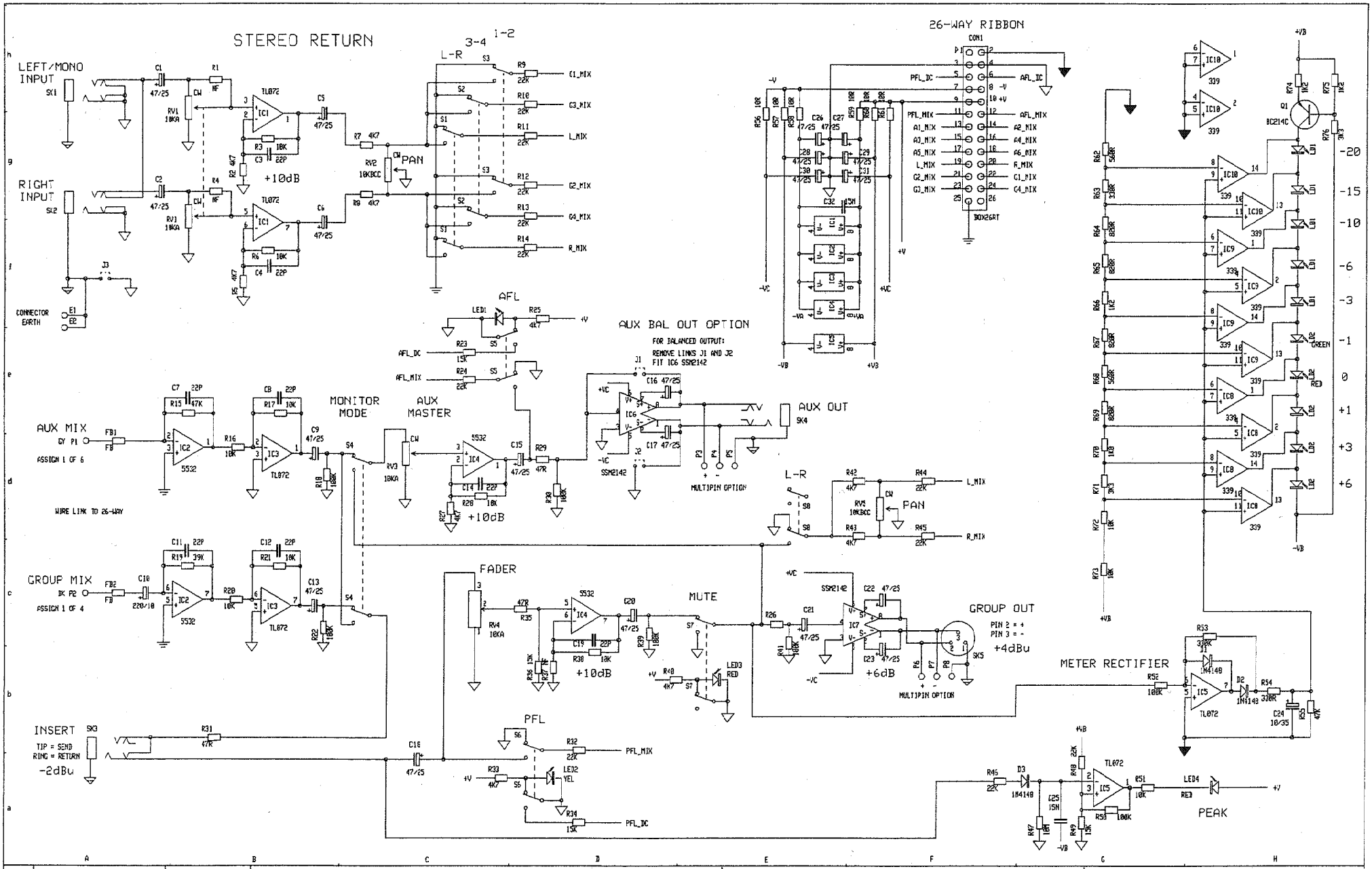
GROUP MODE SWITCHING:

Here the upper block diagram shows the main signal path (in bold) optimised for F-O-H or conventional group/aux operation with the **MONITOR MODE** switch in the up position (flush with panel).

The lower (boxed) block diagram shows the signal path optimised for STAGE MONITOR operation with the **MONITOR MODE** switch pressed (to below panel). In this mode the **AUX MASTER** level control is used to adjust the **AFL** level used to set up the engineers **WEDGE MIX**. For single **AFL** monitoring this control should be set to its '0dB cal' mark.



FW039D GL3 GROUP PCB AG0322 ISSUE 1 COMPONENT LAYOUT
BY CD 3-8-92



ISS.	REVISION	BY	DATE
P1	ORIGIN	CD	17-7-92
1	PRODUCTION	CD	30-8-92
2	IC4 WAS TL072 R37 DELETED	DRP	8-12-93

NOTES

1. RESISTORS MARKED W ARE 1% ALL OTHERS ARE 5% 1/4W UNLESS OTHERWISE MARKED
2. ELECTROLYTIC CAPACITORS ARE µF/VOLTS

GROUP

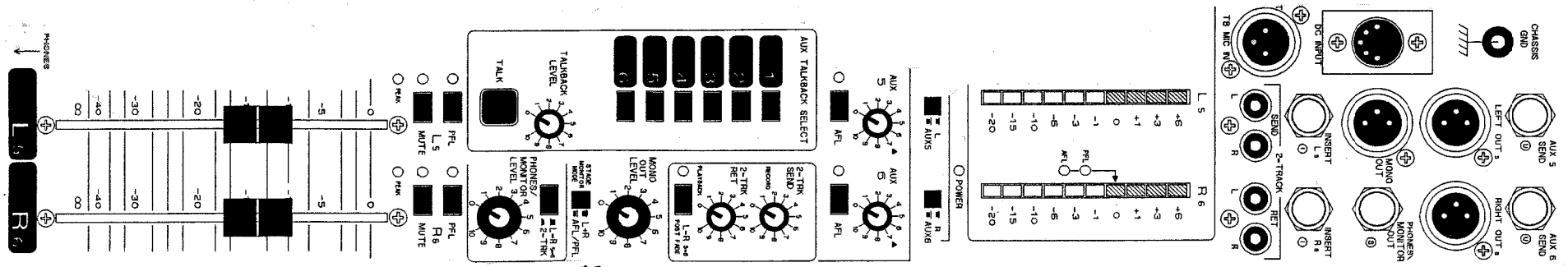
UNIT TITLE: GL3

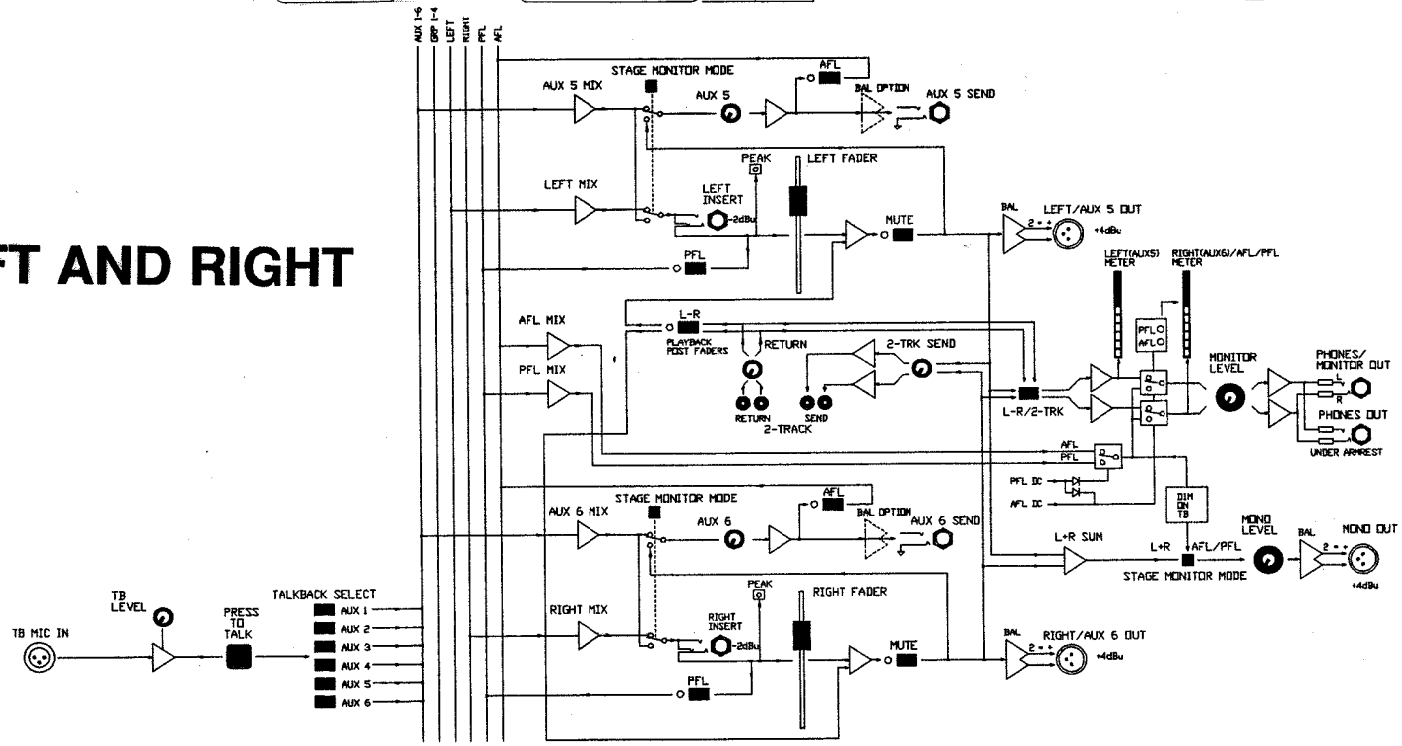
DRAWING TITLE: GROUP CIRCUIT DIAGRAM
PCB TYPE AG0322

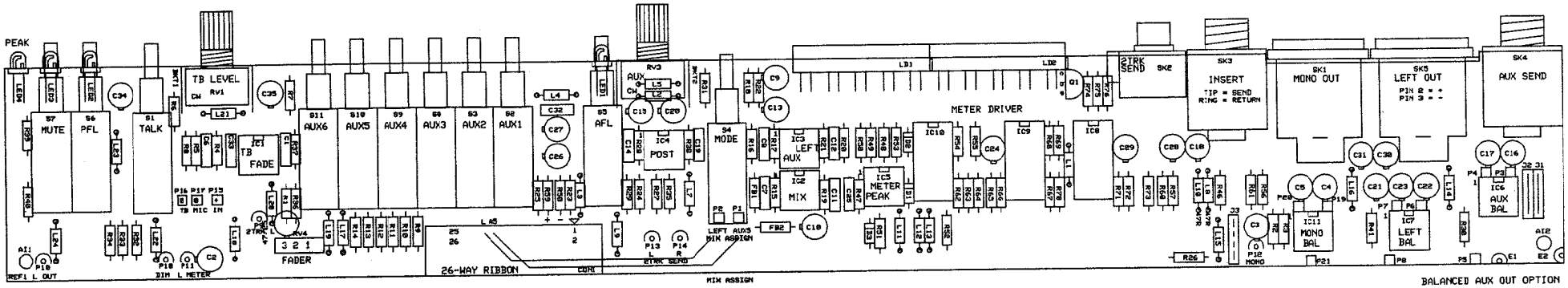
MANUFACTURED IN ENGLAND BY
ALLEN & HEATH

DRAWING No. D177 ISSUE 2

MASTER LEFT AND RIGHT

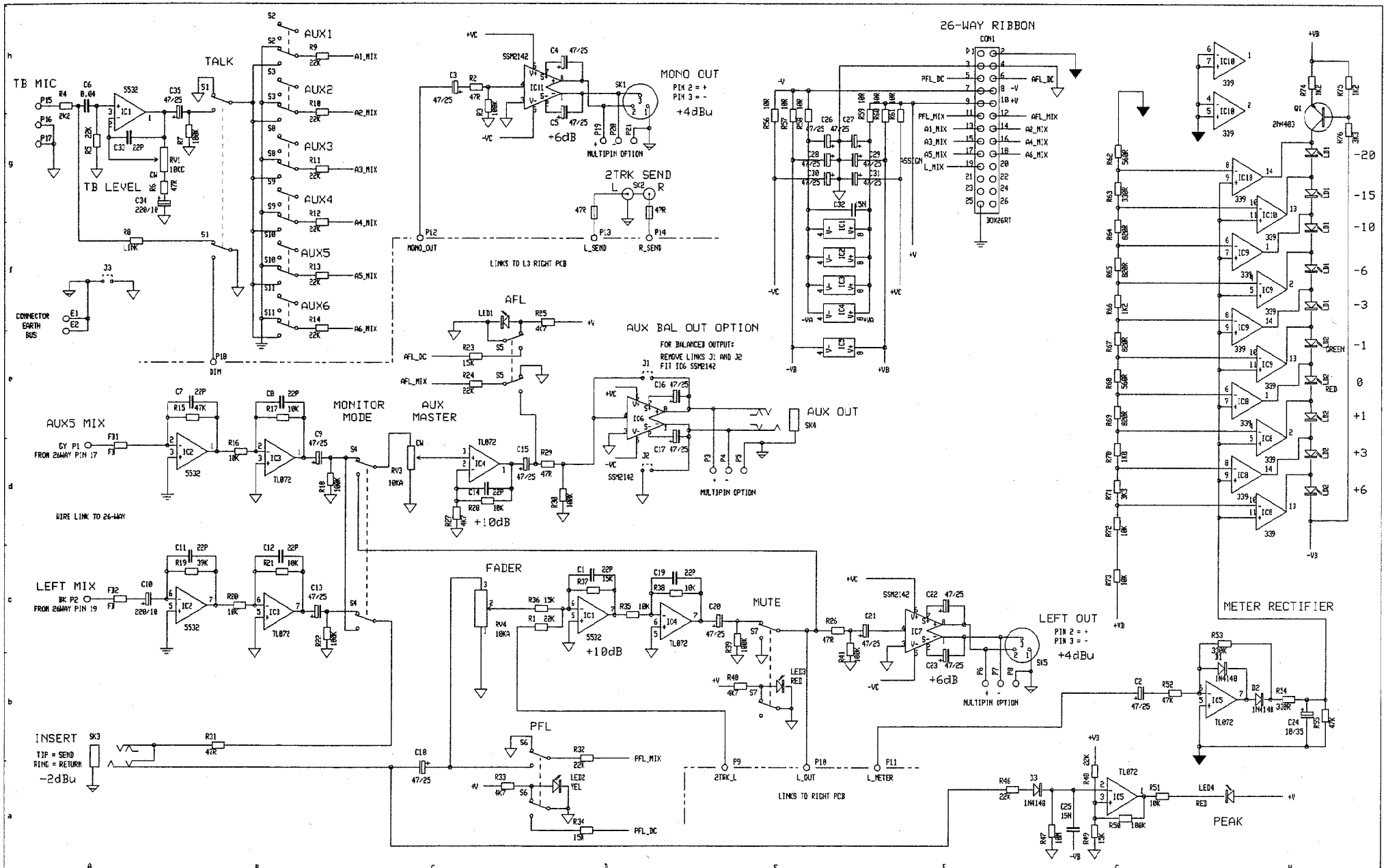






GL3 LEFT PCB AG0323 ISSUE 1 COMPONENT LAYOUT

FW040D



ISS.	REVISION	BY	DATE
P1	ORIGIN	CD	17-7-92
1	PRODUCTION	CD	30-8-92
2	IC1 WAS TL072	DRP	8-12-93

NOTES

1. RESISTORS MARKED # ARE 1% ALL OTHERS ARE 5% 1/4W UNLESS OTHERWISE MARKED
2. ELECTROLYTIC CAPACITORS ARE µF/VOLTS

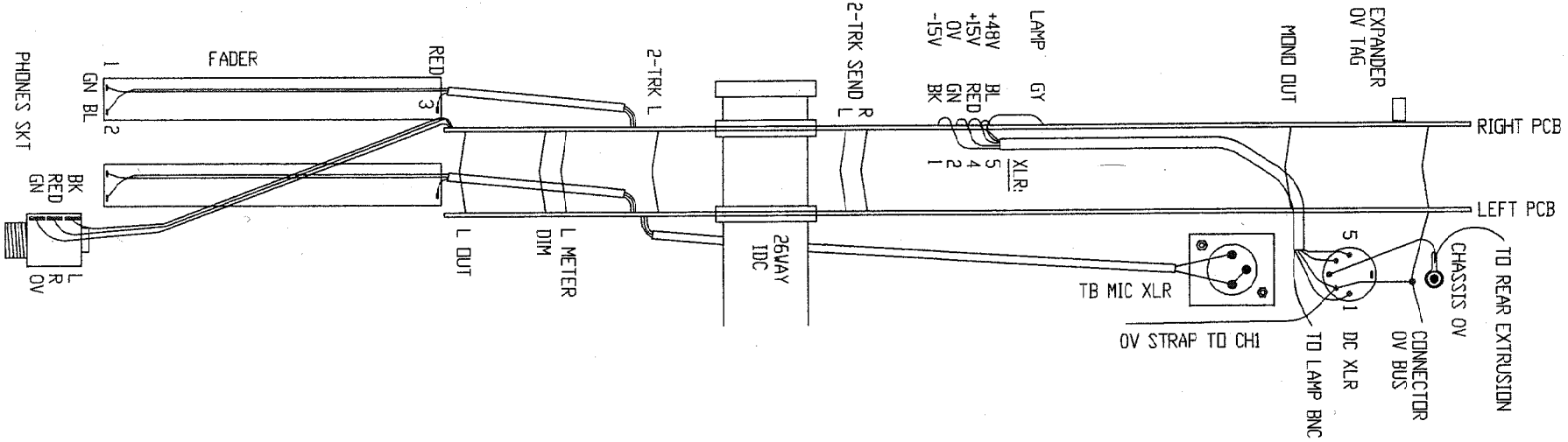
LEFT

UNIT TITLE: GL3

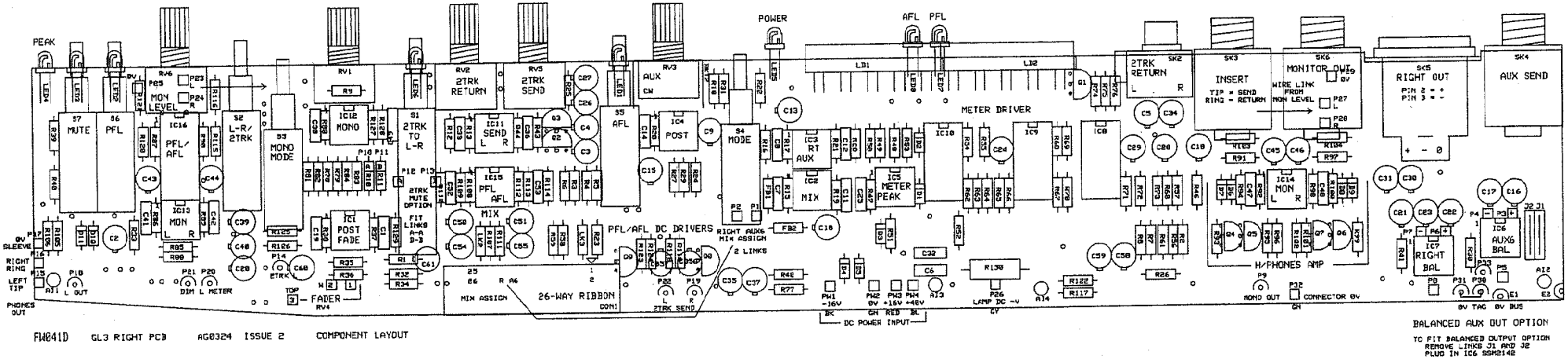
DRAWING TITLE: LEFT CIRCUIT DIAGRAM
PCB TYPE AG0323

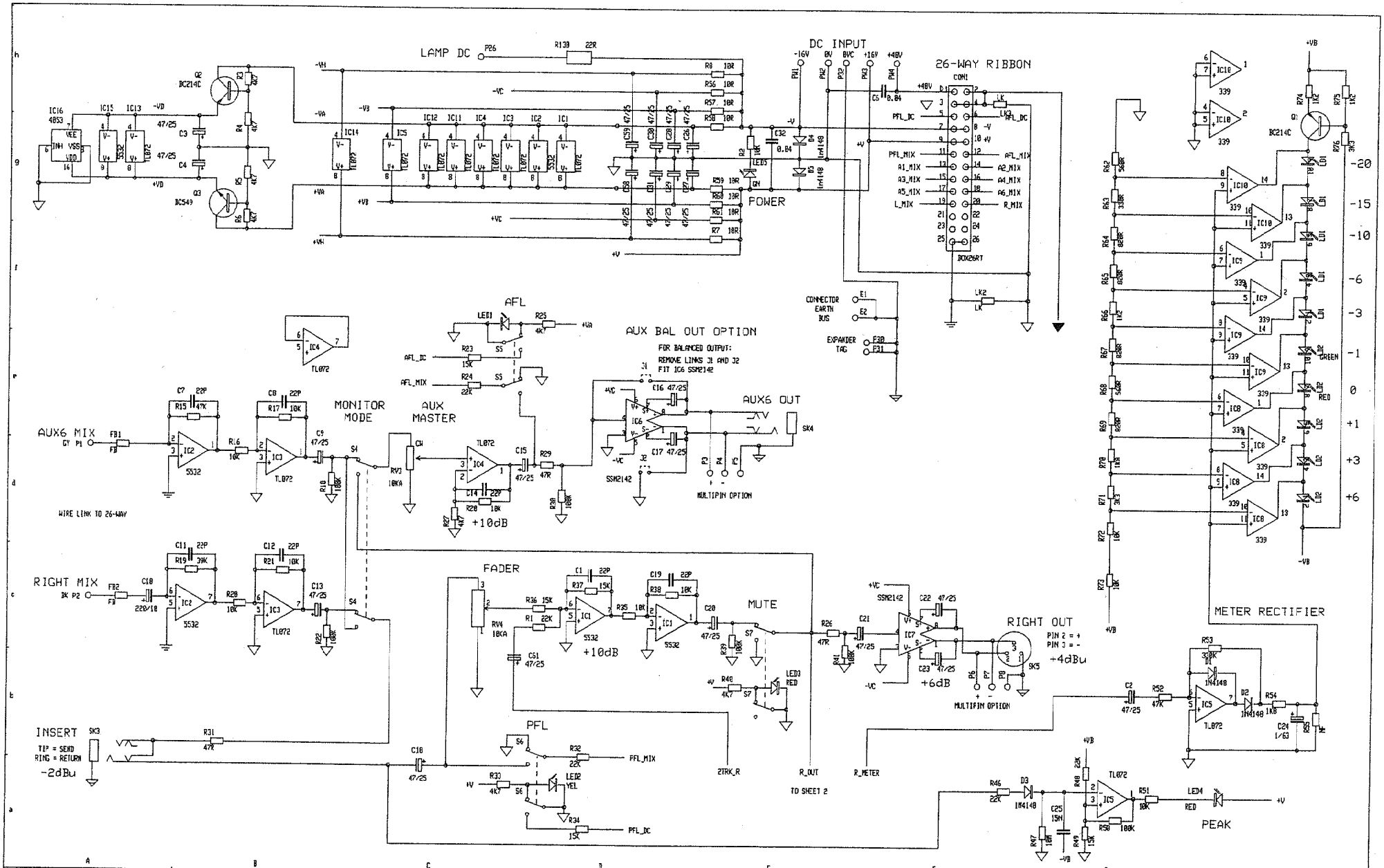
MANUFACTURED IN ENGLAND BY
ALLEN & HEATH

DRAWING No. D178 ISSUE 2



MASTER WIRING





ISS.	REVISION	BY	DATE
1	ORIGIN	CD	19-8-92
2	IC1 WAS TL072 R37 WAS 47K	CD	13-10-92

NOTES

1. RESISTORS MARKED # ARE 1%
   ALL OTHERS ARE 5% 1/4W UNLESS OTHERWISE MARKED
2. ELECTROLYTIC CAPACITORS ARE µF/VOLTS

RIGHT

UNIT TITLE: GL3 SHEET 1 OF 2

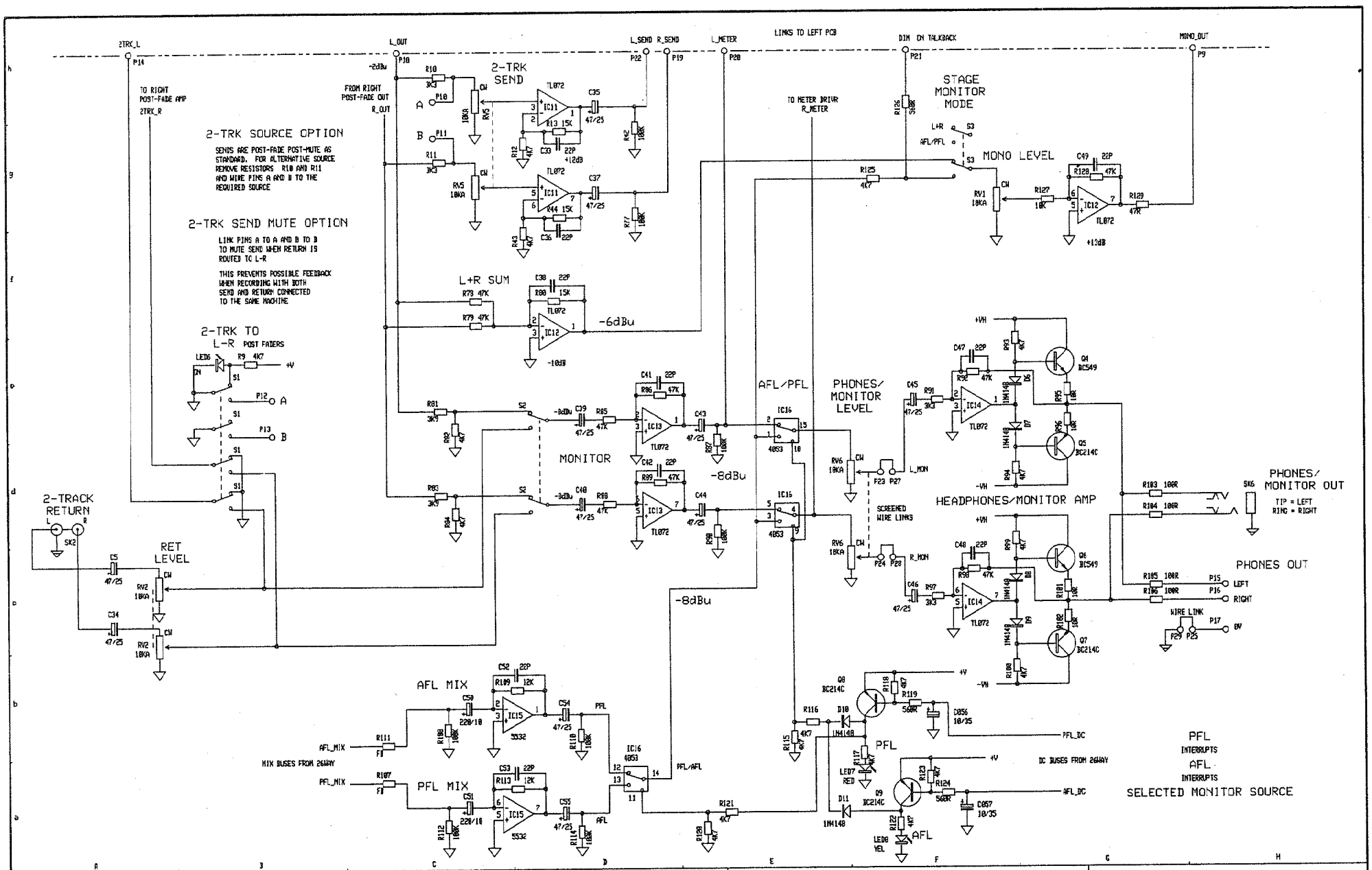
DRAWING TITLE: RIGHT CIRCUIT DIAGRAM

PCB TYPE AG0324

MANUFACTURED IN ENGLAND BY

ALLEN & HEATH

DRAWING No. D179 ISSUE 2



ISS.	REVISION	BY	DATE
P1	ORIGIN	CD	19-8-92

NOTES

1. RESISTORS MARKED * ARE 1% ALL OTHERS ARE 5% 1/4W UNLESS OTHERWISE MARKED
2. ELECTROLYTIC CAPACITORS ARE µF/VOLTS

RIGHT

UNIT TITLE GL3 SHEET 2 OF 2

DRAWING TITLE RIGHT CIRCUIT DIAGRAM

PCB TYPE AG0324

MANUFACTURED IN ENGLAND BY

ALLEN & HEATH

DRAWING No. D179 ISSUE 1