

**Released Engineering Change Notice**

Product	iDR-0
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Title	Backplane CPLD code update to 1.1.
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Date	10 <sup>TH</sup> OCTOBER 2008
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**Details Of Change**

Update iDR0 backplane CPLD code to version 1.1. Code already in production programmable devices.  
 Info for tech support: iDR-Live firmware version 1.4 and later will include a hardware report with any event logs sent in by customers. If the customer has an iDR0 that does not have this updated CPLD code (i.e. any already manufactured unit), the CPU card ID will be reported incorrectly, and will not have the correct description in this report. This does not imply a problem with the CPU card. CPU config links: May not be possible to set individual config links, with the exception of number 8 (moving one link to 'on' may activate the next link as well, e.g. setting link 5 to 'on' may actually turn on links 5 AND 6).

**Reason for change**

Bug in existing CPLD causes incorrect reading of CPU card ID and CPU config links.

This ECN supersedes ECN No.	
	Related ECNs

Post Implementation Ser No		
Serial Number	Model	Date
IDR0-002000	IDR-0	12/11/08

Serial Number	Model	Date

**Additional Information**