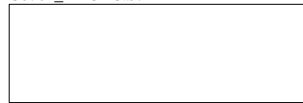


IDR 0 & 2 PSU PCB  
AG6701 Issue 1

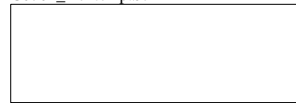
TOP OVERLAY

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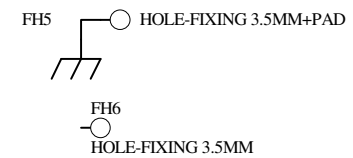
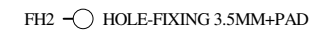
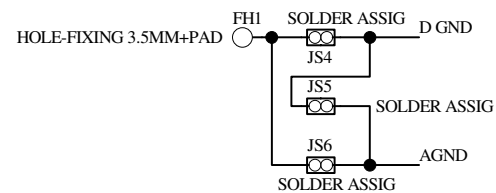
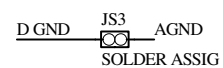
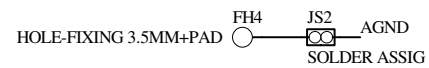
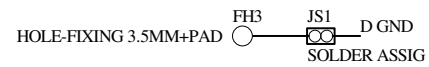
C6701\_1 ACDC  
C6701\_1 ACDC.Sch



C6701\_1 extemp  
C6701\_1 extemp.Sch



All solder assignments to be connected.



- AI1 ○ HOLE-AI
- AI2 ○ HOLE-AI
- AI3 ○ HOLE-AI
- AI4 ○ HOLE-AI
- BO1 ○ PCB BREAKOUT S
- BO2 ○ PCB BREAKOUT S

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| ISSUE | BY   | DATE     | CHK BY |
|-------|------|----------|--------|
| A     | IMcB | 24/11/06 |        |
| B     | IMcB | 14/8/07  |        |
| 1     | McB  | 23/10/07 |        |

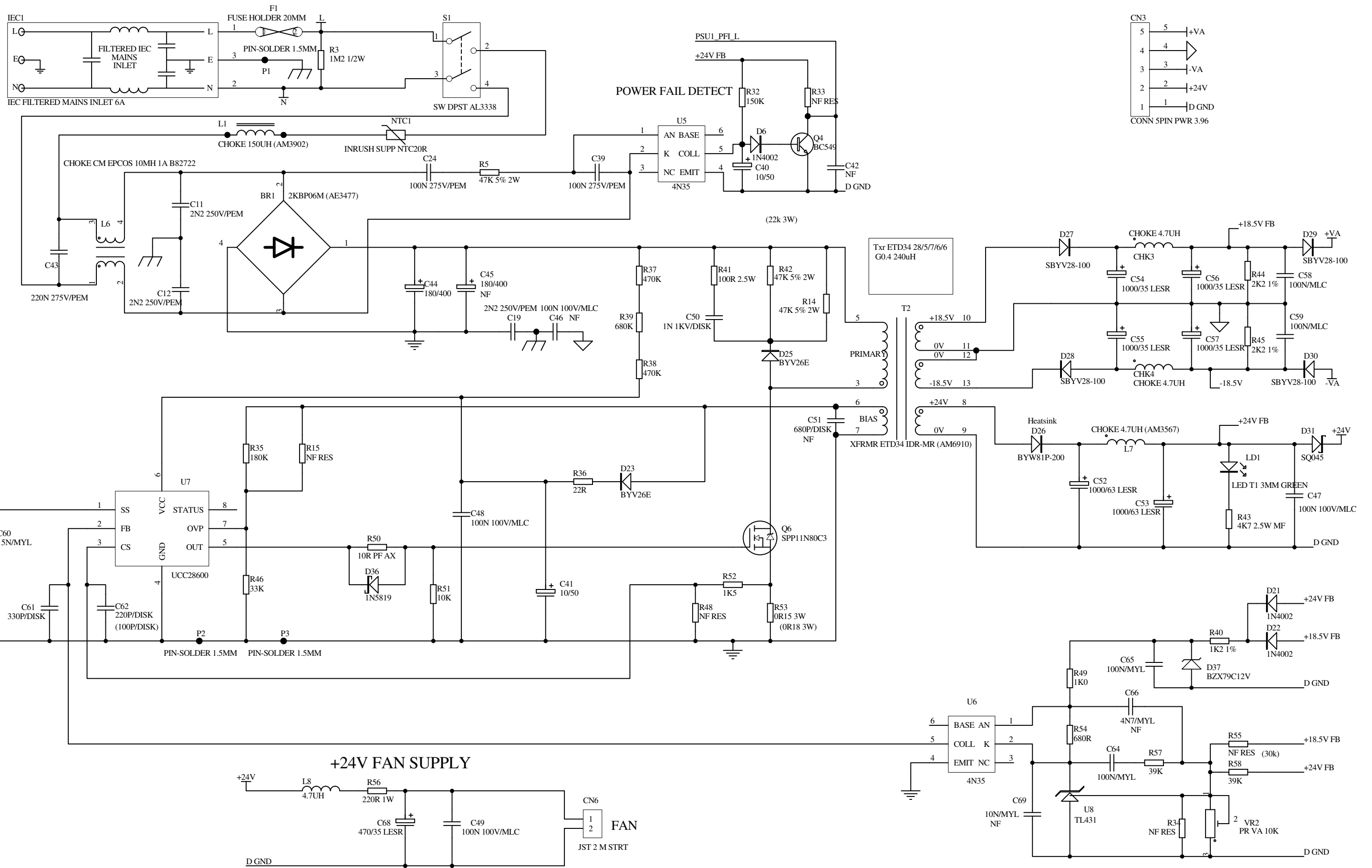
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|-------------------|-------------------------------|------------------------------------|

TITLE: IDR 00/02 PSU

PAGE:

DRG No: AG6701 ISSUE: 1 SHEET: 1 OF 3

A B C D E F G H



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|-------|------|----------|--------|
| A     | IMcB | 24/11/06 |        |
| B     | IMcB | 14/08/07 |        |
| 1     | McB  | 23/10/07 |        |

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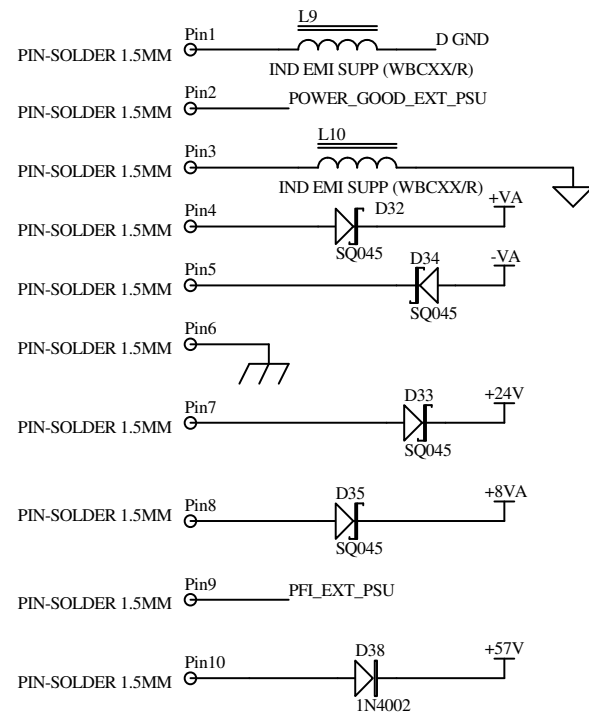
\* All parts to be RoHS Compliant \*

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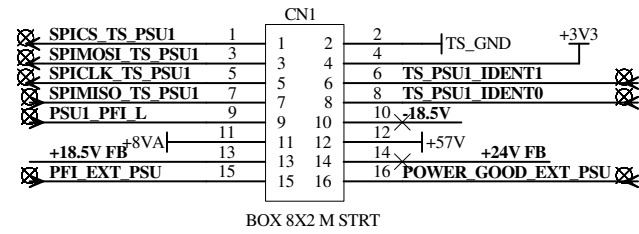
TITLE: **iDR 00/02 PSU**  
PAGE: **MAIN PSU**

DRG No: **AG6701** ISSUE: **1** SHEET: **2 OF 3**

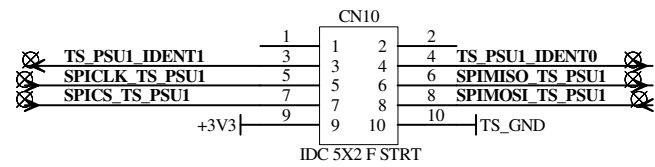
### EXTERNAL PSU CONNECTOR



### TEMP SENSING TO BACKPLANE PCB

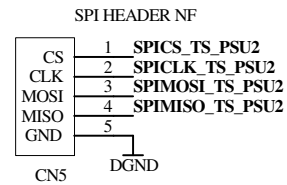


### TEMPERATURE SENSING ON PSU PCB

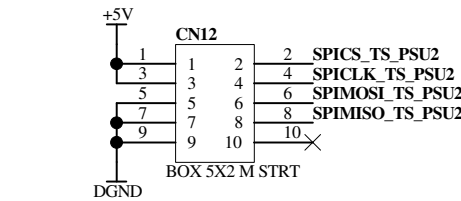


## PSU PCB

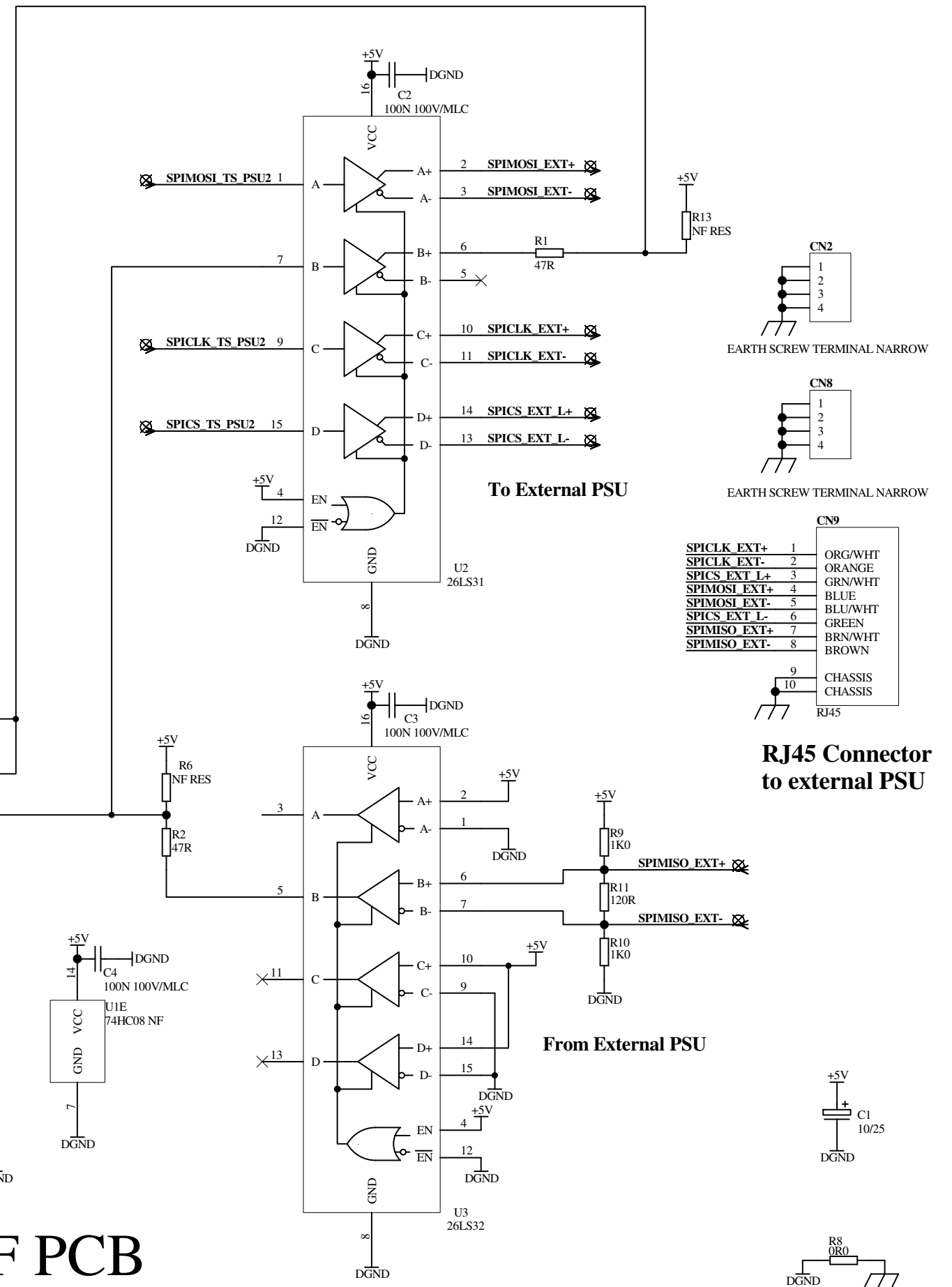
### SPI Test Header



### Connector to Backplane



## BREAK OFF PCB



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|-------|------|----------|--------|
| A     | DLP  | 26/04/07 |        |
| B     | IMcB | 14/8/07  |        |
| 1     | IMcB | 23/10/07 |        |

FILE: C6701\_1 extemp.Sch

\* All parts to be RoHS Compliant \*

PRINTED: 14:26:00 28-Jan-2008

TITLE: IDR 00/02 PSU  
PAGE: External PSU SPI Interface

DRG No: AG6701 ISSUE: 1 SHEET: 3 OF 3